

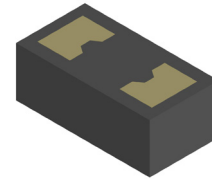
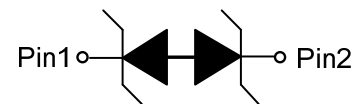
ESD54191CZ
1-Line, Bi-directional, Transient Voltage Suppressors
<http://www.sh-willsemi.com>
Descriptions

The ESD54191CZ is a TVS (Transient Voltage Suppressor) designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by ESD (Electrostatic Discharge).

The ESD54191CZ incorporates one pair of low capacitance diodes.

The ESD54191CZ may be used to provide ESD protection up to $\pm 30\text{kV}$ (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 13A (8/20 μs) according to IEC61000-4-5.

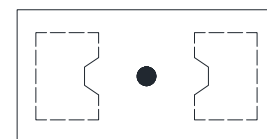
The ESD54191CZ is available in DWN0603-2L package. Standard products are Pb-free and Halogen-free.


DWN0603-2L (Bottom View)

Pin configuration
Features

- Stand-off voltage: $\pm 5.5\text{V}$ Max.
- Transient protection for each line according to IEC61000-4-2 (ESD): $\pm 30\text{kV}$ (contact discharge)
IEC61000-4-5 (surge): 13A (8/20 μs)
- Low capacitance: $C_J = 20\text{pF}$ typ.
- Low leakage current: $I_R < 1\text{nA}$ typ.
- Low clamping voltage: $V_{CL} = 9\text{V}$ typ. @ $I_{PP} = 16\text{A}$ (TLP)
- Solid-state silicon technology

Applications

- Cellular handsets
- Tablets
- Laptops
- Other portable devices
- Network communication devices



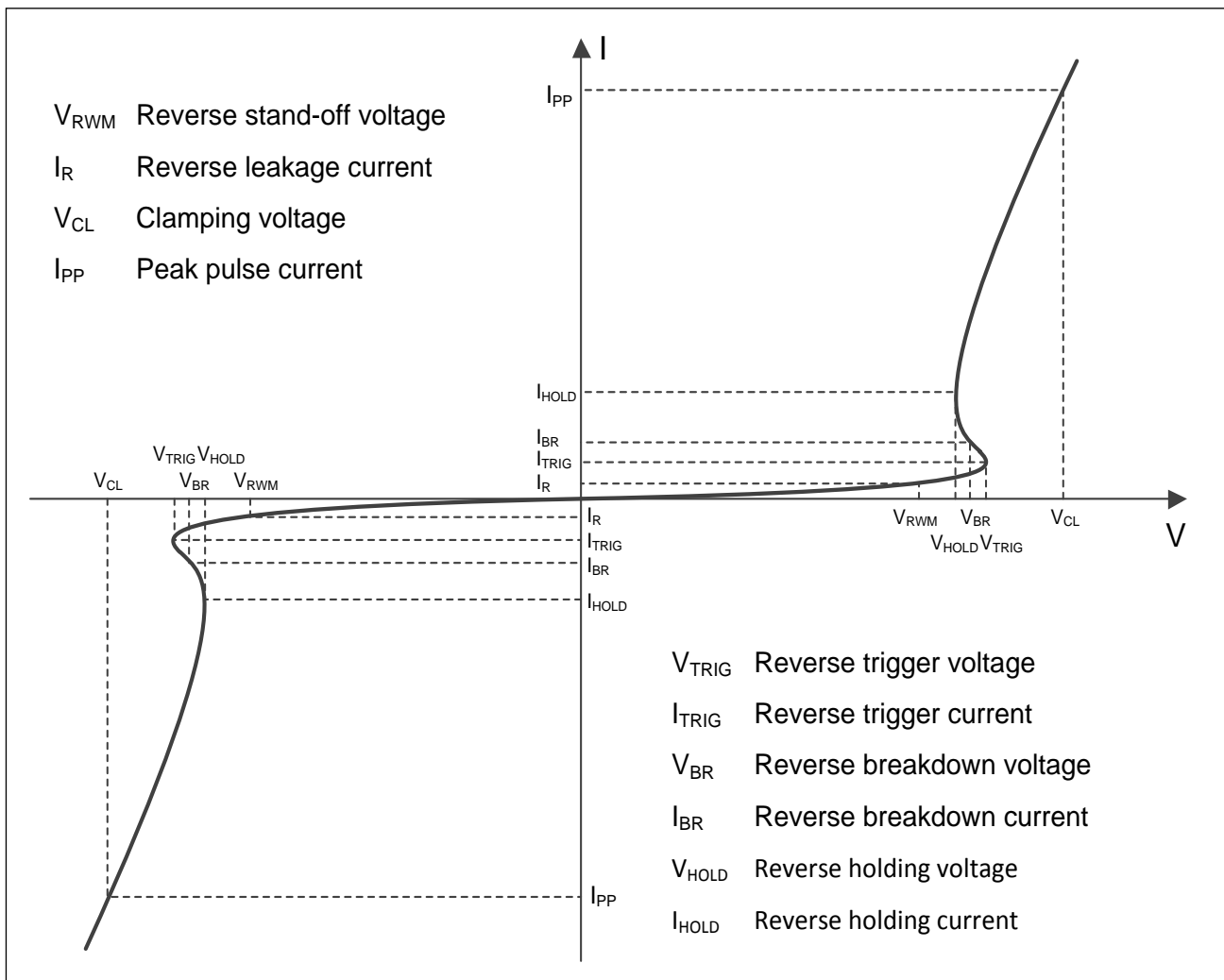
• = Device code

Marking (Top View)
Order information

Device	Package	Shipping
ESD54191CZ-2/TR	DWN0603-2L	10000/Tape&Reel

Absolute maximum ratings

Parameter	Symbol	Rating	Unit
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	182	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	13	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	kV
ESD according to IEC61000-4-2 contact discharge		± 30	
Junction temperature	T_J	125	$^{\circ}C$
Operating temperature	T_{OP}	-40~85	$^{\circ}C$
Lead temperature	T_L	260	$^{\circ}C$
Storage temperature	T_{STG}	-55~150	$^{\circ}C$

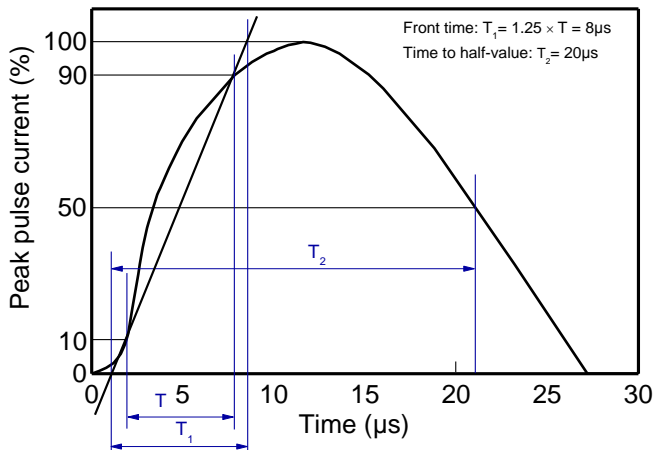
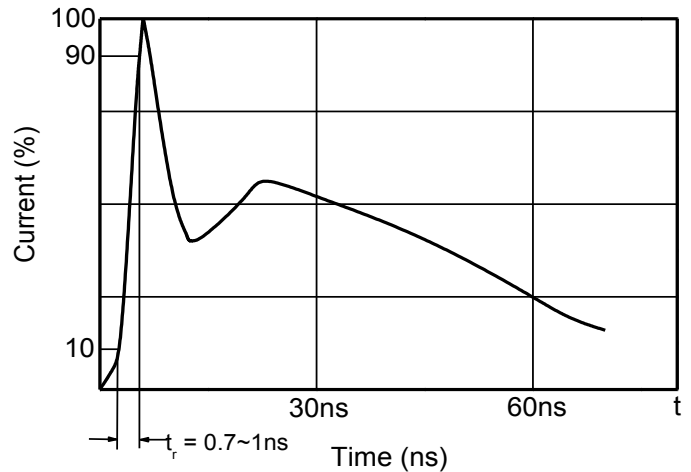
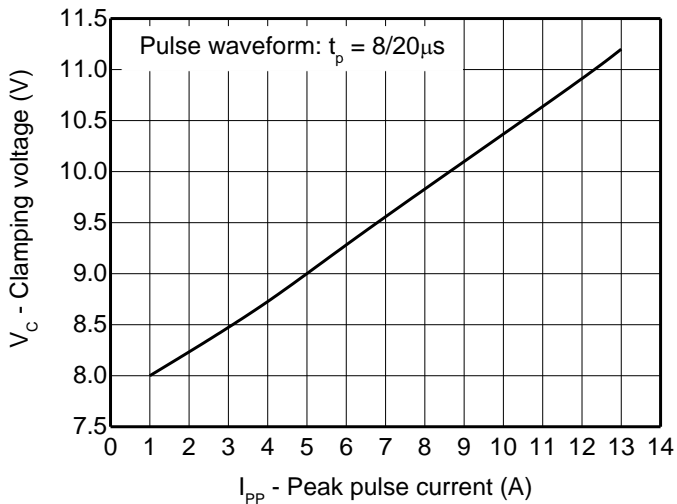
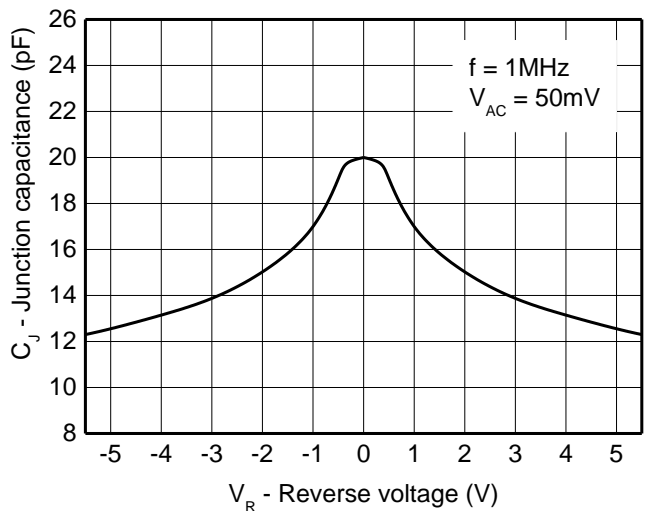
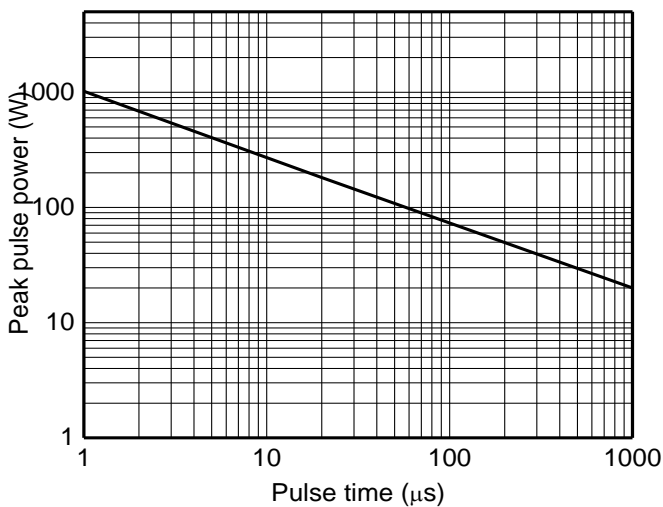
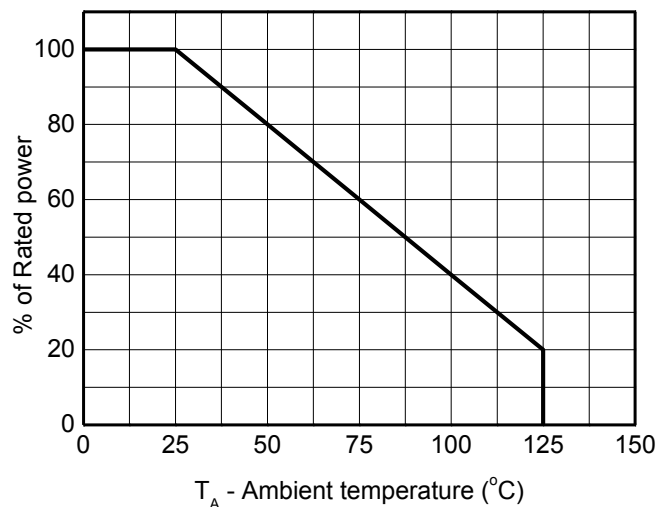
Electrical characteristics ($T_A=25^{\circ}C$, unless otherwise noted)

Definitions of electrical characteristics

Electrical characteristics (T_A=25 °C, unless otherwise noted)

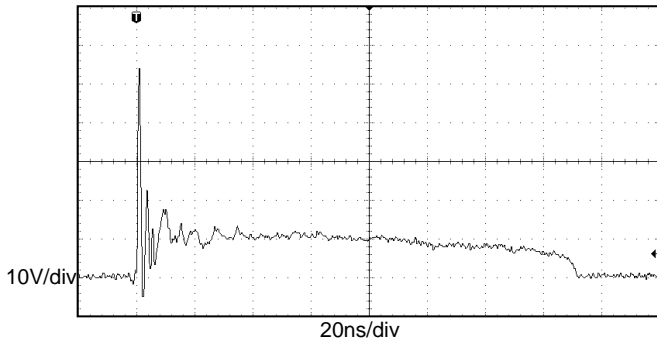
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse maximum working voltage	V _{RWM}				±5.5	V
Reverse leakage current	I _R	V _{RWM} = 5.5V		<1	50	nA
Reverse breakdown voltage	V _{BR}	I _T = 1mA	6.5	8.1	9.0	V
Clamping voltage ¹⁾	V _{CL}	I _{PP} = 16A, t _p = 100ns		9		V
Dynamic resistance ¹⁾	R _{DYN}			0.08		Ω
Clamping voltage ²⁾	V _{CL}	V _{ESD} = 8kV		10		V
Clamping voltage ³⁾	V _{CL}	I _{PP} = 1A, t _p = 8/20μs			10	V
		I _{PP} = 13A, t _p = 8/20μs			14	V
Junction capacitance	C _J	V _R = 0V, f = 1MHz		20	25	pF

Notes:

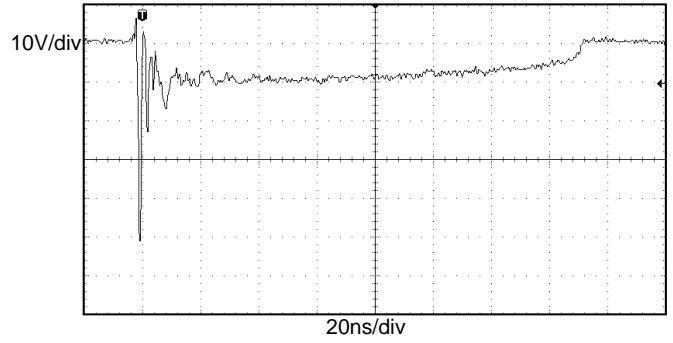
- 1) TLP parameter: Z₀ = 50Ω, t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
- 2) Contact discharge mode, according to IEC61000-4-2.
- 3) Non-repetitive current pulse, according to IEC61000-4-5.

Typical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

8/20 μs waveform per IEC61000-4-5

Contact discharge current waveform per IEC61000-4-2

Clamping voltage vs. Peak pulse current

Capacitance vs. Reverse voltage

Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

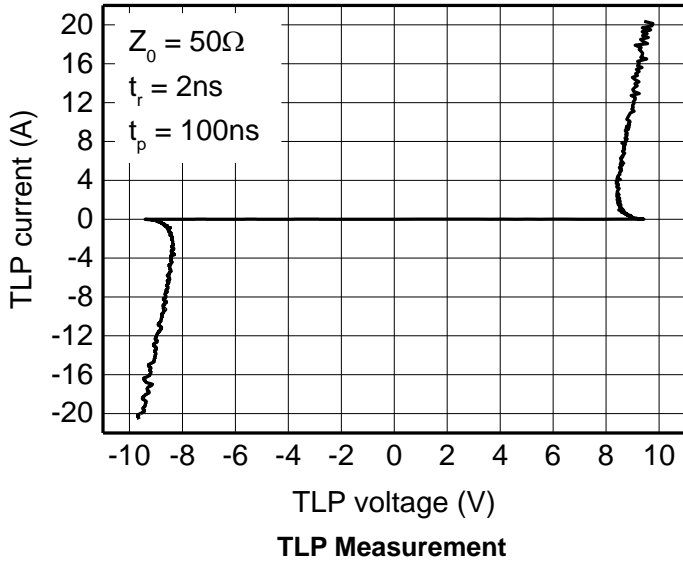
Typical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)

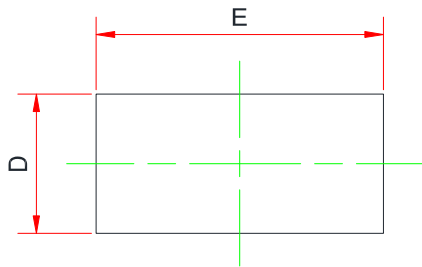
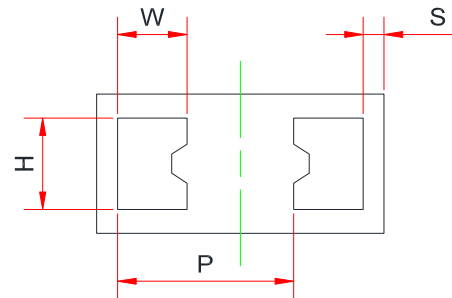
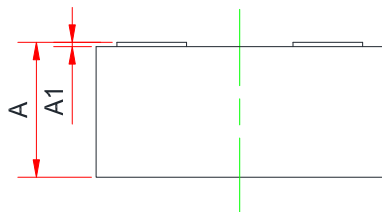


ESD clamping
 (+8kV contact discharge per IEC61000-4-2)

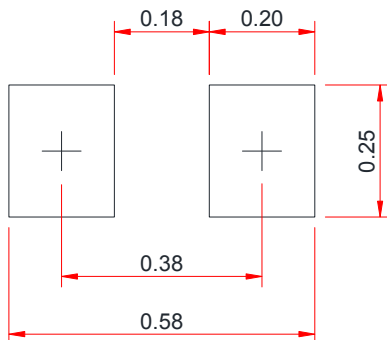


ESD clamping
 (-8kV contact discharge per IEC61000-4-2)

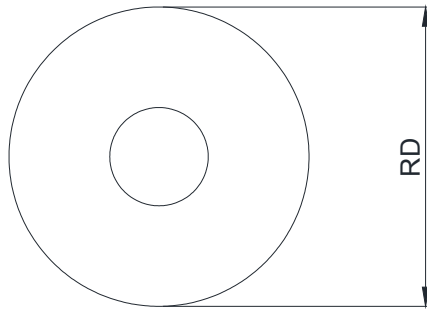
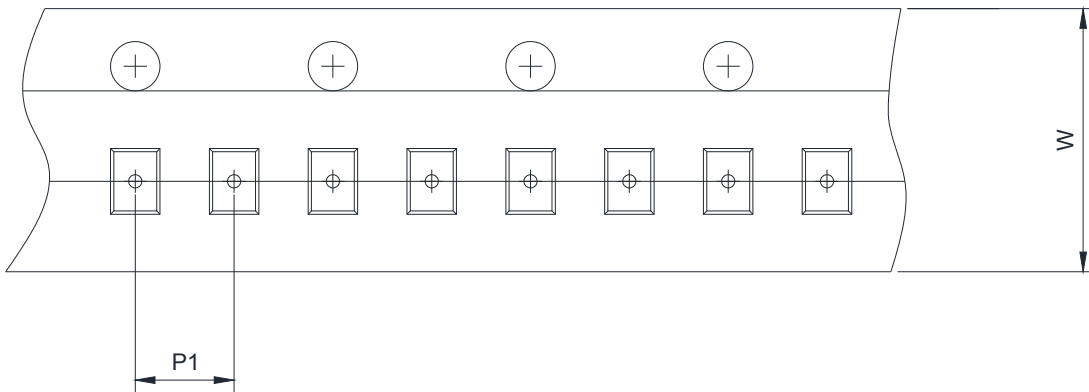
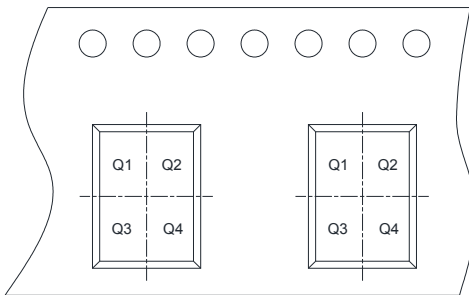


PACKAGE OUTLINE DIMENSIONS
DWN0603-2L

Top View

Bottom View

Side View

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.268	0.310	0.352
A1	0.000	-	0.005
D	0.290	0.320	0.350
E	0.590	0.620	0.650
W	0.150 Ref.		
H	0.210 Ref.		
S	0.045 Ref.		
P	0.380 Ref.		

Recommend land pattern (Unit: mm)

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

TAPE AND REEL INFORMATION
Reel Dimensions

Tape Dimensions

Quadrant Assignments For PIN1 Orientation In Tape



 User Direction of Feed

RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch <input type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm <input type="checkbox"/> 12mm
P1	Pitch between successive cavity centers	<input checked="" type="checkbox"/> 2mm <input type="checkbox"/> 4mm <input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1 <input type="checkbox"/> Q2 <input type="checkbox"/> Q3 <input type="checkbox"/> Q4